TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (DTMOS II)

TK15A60U

Switching Regulator Applications

Unit: mm

Low drain-source ON-resistance: R_{DS} (ON) = 0.24 Ω (typ.)

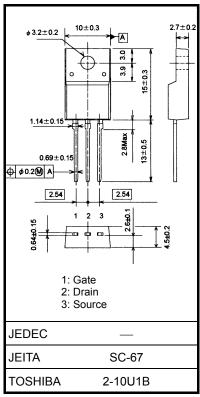
High forward transfer admittance: |Y_{fS}| = 8.5 S (typ.)

Low leakage current: I_{DSS} = 100 μA (V_{DS} = 600 V)

• Enhancement mode: V_{th} = 3.0 to 5.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	600	V
Gate-source voltage		V_{GSS}	±30	V
Drain current	DC (Note 1)	I _D	15	
	Pulse (t = 1 ms) (Note 1)	I _{DP}	30	Α
Drain power dissipation (Tc = 25°C)		P _D	40	W
Single pulse avalanche energy (Note 2)		E _{AS}	81	mJ
Avalanche current (Note 3)		I _{AR}	15	Α
Repetitive avalanche energy		E _{AR}	4	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature range		T _{stg}	-55 to 150	°C



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of

high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

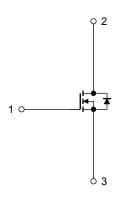
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	3.125	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 0.63 mH, R_G = 25 Ω , I_{AR} = 15 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



Electrical Characteristics (Ta = 25°C)

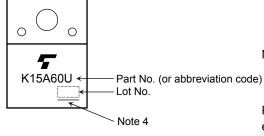
Charac	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage curre	nt	I _{GSS}	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА
Drain cut-off currer	t	I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V	_	_	100	μА
Drain-source break	down voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	600	_	_	V
Gate threshold volt	age	V _{th}	V _{DS} = 10 V, I _D = 1 mA	3.0	_	5.0	V
Drain-source ON-re	esistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 7.5 A		0.24	0.3	Ω
Forward transfer ad	dmittance	Y _{fs}	V _{DS} = 10 V, I _D = 7.5 A	3.0	8.5	_	S
Input capacitance		C _{iss}		_	950	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	47	_	pF
Output capacitance		C _{oss}		_	2300	_	
Switching time	Rise time	t _r	$\begin{array}{c c} 10 \text{ V} & \text{I}_D = 7.5 \text{A} & \text{V}_{OUT} \\ \hline \text{VGS} & \text{O} & \text{V} & \text{RL} = \\ 50 \Omega & \text{W}_{DD} \simeq 300 \text{ V} \\ \\ \text{Duty} \leq 1\%, t_W = 10 \mu\text{s} \\ \end{array}$	_	37	_	ns
	Turn-ON time	t _{on}		_	80	_	
	Fall time	t _f			8		
	Turn-OFF time	t _{off}			105		
Total gate charge		Qg		_	17	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}$	_	10	_	nC
Gate-drain charge		Q _{gd}		_	7	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	15	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	30	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 15 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 15 A, V _{GS} = 0 V,	_	530	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 100 A/μs	_	9.0	_	μС

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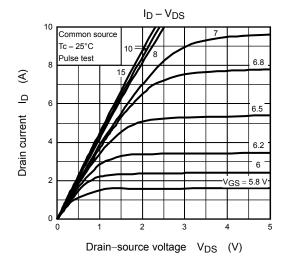
Marking

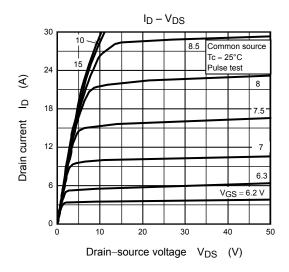


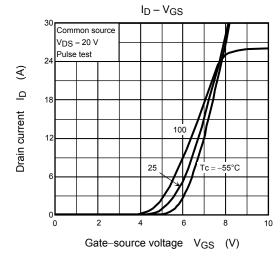
Note 4: A dot marking for identifying the indication of product Labels.

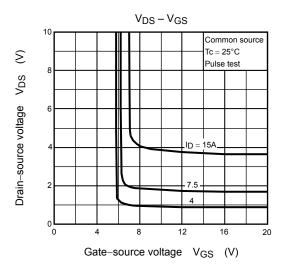
[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

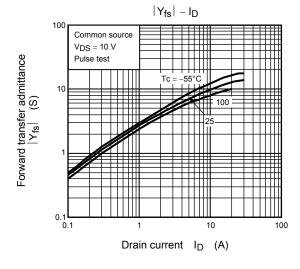
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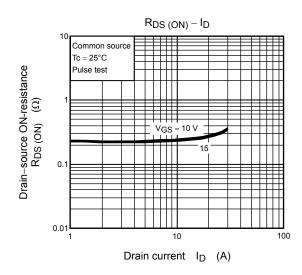




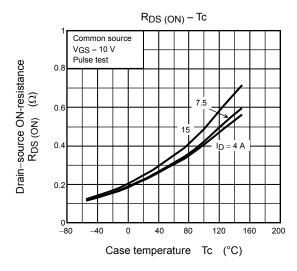


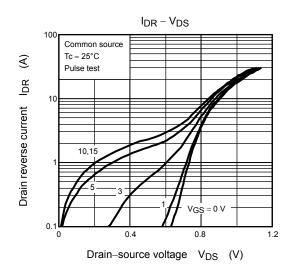


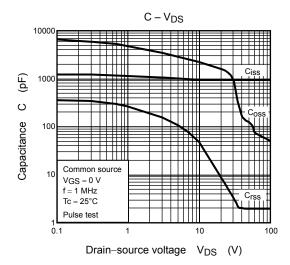


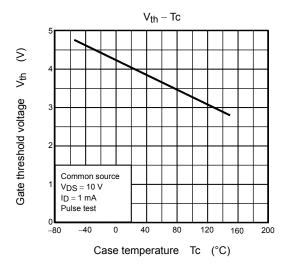


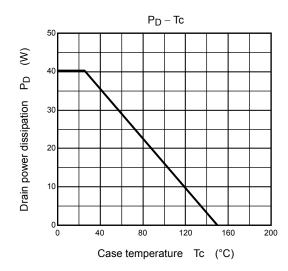
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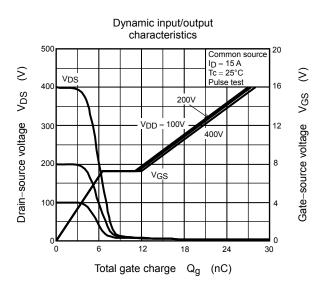


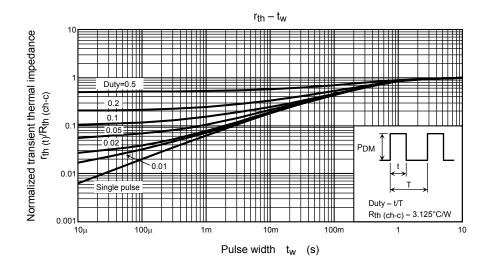


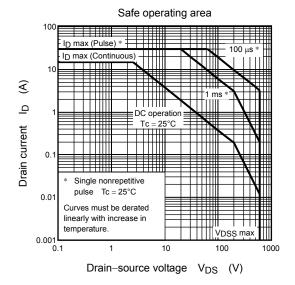


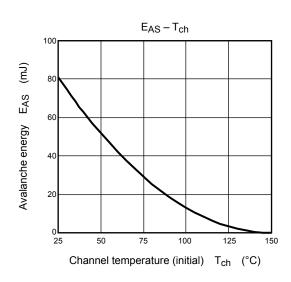


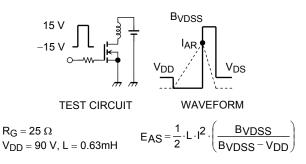












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